

ABSTRACT OF THE DISCLOSURE

An interface circuit includes an interface communication section for performing communication with an upstream device; a system interface communication section for performing communication with a system controller; and an interface control section for controlling these sections. The interface control section includes a section for executing first reset processing; a section for requesting the system interface communication section to request the system controller to permit execution of second reset processing different from the first reset processing; a section for requesting the system interface communication section to receive a report of the permission of the execution of the second reset processing from the system controller; a section for executing the second reset processing in accordance with the report of the permission of the execution of the second reset processing; and a section for requesting the interface communication section to report completion of the second reset processing to the upstream device.